

ISOCC 2020 Program Schedule At-a-glance

WEDNESDAY_ OCTOBER 21, 2020

		Lobby	LOBBY	GRAND BALLROOM2	SAPPHIRE1	SAPPHIRE2	SAPPHIRE3
From	Till		1F	1F	2F	2F	2F
12:00	13:00	On-site Registration					
13:00	14:30			Tutorial 1			
14:30	16:00			Tutorial 2			
16:00	17:30			Tutorial 3			
17:30	18:00		Break Time (30min.)				
18:00	20:00		Welcome Reception (Grand Ballroom 1)				

THURSDAY_ OCTOBER 22, 2020

		Lobby	LOBBY	GRAND BALLROOM2	SAPPHIRE1	SAPPHIRE2	SAPPHIRE3			
From	Till		1F	1F	2F	2F	2F			
08:30	08:40	On-site Registration	Chip Design Contest (CDC) Poster 1		CDC Oral					
08:40	08:50									
08:50	09:00									
09:00	09:10									
09:10	09:20									
09:20	09:30				Break Time (10min.)					
09:30	09:45				Opening Ceremony (Grand Ballroom1)					
09:45	10:25				Keynote Speech-1 (Grand Ballroom1)					
10:25	11:05				Keynote Speech-2 (Grand Ballroom1)					
11:05	11:20				Break Time (15min.)					
11:20	12:00			Keynote Speech-3 (Grand Ballroom1)						
12:00	13:15			Lunch (5F Lily 1)						
13:15	13:30			DC	16	DCAS1	216	236	SS1	1
13:30	13:45				181		134	186		3
13:45	14:00				86		220	200		6
14:00	14:15				99		97	218		12
14:15	14:30				101		111	52		112
14:30	14:45			Break Time (15min.)						
14:45	15:00			RF1	51	WLN	137	8	SS3	23
15:00	15:15				74		163	9		66
15:15	15:30				184		162	109		67
15:30	15:45				193		211	168		68
15:45	16:00				203		223	195		94
16:00	16:15			Chip Design Contest (CDC) Poster Exhibition						
16:15	16:30							227		
16:30	16:45				ET1	88	183	SS2	15	
16:45	17:00					47	81		19	
17:00	17:15					188	92		27	
17:15	17:30					78	225		30	
17:30	17:45					230	91		45	
17:45	18:00		Break Time (15min.)							
18:00	20:00		Banquet							



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FRIDAY, OCTOBER 23, 2020

		Lobby	LOBBY	GRAND BALLROOM2	SAPPHIRE1	SAPPHIRE2	SAPPHIRE3		
From	Till		1F	1F	2F	2F	2F		
09:00	09:15	On-site Registration		60	10	24	70		
09:15	09:30			140	44	46	104		
09:30	09:45		RF2	142	SoC2	Power Management	143	133	
09:45	10:00			179		187	234		
10:00	10:15			199		219	235		
10:15	10:30		Break Time (15min.)						
10:30	11:10		Keynote Speech-4 (Grand Ballroom1)						
11:10	11:50		Keynote Speech-5 (Grand Ballroom1)						
11:50	12:00		Break Time (10min.)						
12:00	12:40				Short Tutorial 1	Short Tutorial 2			
12:40	14:00		Lunch (5F Lily 1)						
14:00	14:15		Intro. of Research Lab. (Poster)	DCAS2	32	25	65	28	
14:15	14:30				108	35	75	41	
14:30	14:45				33	ML3	39	80	42
14:45	15:00				40	175	89	61	
15:00	15:15				228	177	102	180	
15:15	15:30		Break Time (15min.)						
15:30	14:45		Poster Session		161	107	226		
15:45	16:00				20	113	231		
16:00	16:15				72	114	232		
16:15	16:30			212	178	237			
16:30	16:45			98	202	238			
16:45	17:00	Break Time (15min.)							
17:00	17:30	Closing Ceremony							

SATURDAY, OCTOBER 24, 2020

10:00	11:00	Committee Meeting & Discussion
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Regualr Session	Title
DC	Advanced Data Converters
DCAS1	Circuit and Systems for Memory Applications
ML1	Machine Learning to Real Applications
RF1	RF and Wireless Circuits I
WLN	Wireline
SoC1	Software and Algorithm
Power Management	Power Mangement
ET1	Emerging Technologies for Systems
ML2	HW/SW Co-optimization in Machine Learning
RF2	RF and Wireless Circuits II
SoC2	HW-SW Co-design
DCAS2	Circuits and Systems for Multimedia Applications
ML3	Special Hardwares for Machine Learning
Analog Circuit 1	Analog Circuits1
Analog Circuit 2	Analog Circuits2
ET2	Emerging Technologies for New Computing

Special Session	Title
SS 1	Design, Analysis and Tools for Integrated Circuits and Systems (DATICS)
SS 2	Design and Analysis of Nonlinear Circuits and Networks
SS 3	Edge Computing for Smart IoT Systems: Device, Circuit, and System
SS 4	Advanced Algorithms and Hardware Accelerators
SS 5	Circuits and theories for neuromorphic, non-linear, and sensing techniques
SS 6	Artificial Intelligent and Security Systems