



Call for Papers ISOCC 2012, Theme : SoC Design for Smart Living

2012 International SoC Design Conference November 4-7, 2012 | Ramada Plaza Hotel, Jeju, Korea



International SoC Design Conference (ISOCC) aims at providing the world's premier SoC design forum for leading researchers from academia and industries. Prospective authors are invited to submit papers of their original works emphasizing contributions beyond the present state of the art. ISOCC 2012 is technically co-sponsored by **IEEE CAS** Society and accepted papers will be published on **IEEE Xplore**. We also welcome proposals on special sessions.

Paper Submission

Complete 2-page to 4-page manuscript (in Standard IEEE double-column format) is requested. Papers must be submitted electronically in PDF format. Only electronic submission will be accepted. For more information, please refer to the conference website: <http://www.isocc.org>.

Areas of Interest

Analog and Mixed-Signal Circuits	Communication SoCs
Display Driver and Imaging Devices	Embedded Memories
Embedded System Software	High Speed Signal Interfaces
Low Power Design Techniques	Microprocessor and DSP Architectures
Energy-Aware Systems	SoC Design Methodology
Multimedia (A/V) SoCs	SoCs for Automotive Technology
Wireline & Wireless ICs (RF ICs)	Sensor & MEMS
Signal Integrity/Interconnect Modeling	Power Electronics (Energy Harvesting)
SoC Testing and Verification	Bio & Medical Devices

Special Sessions

Proposals are solicited for special sessions. Please submit proposals for special sessions to the special session chair.

Chip Design Contest

Design contest provides the academia with the opportunity to introduce their novel chip designs to the real world. The selected designs will be awarded. Papers should be submitted in electronic form via http://www1.idec.or.kr/conference/conference_isocc.asp.

Best Paper Awards

The authors of selected papers will be awarded for technical contributions and their papers will be invited for publication in the Journal of Semiconductor Technology and Science (SCIE) published by Institute of Electronic Engineers of Korea (IEEK). (Visit <http://www.jsts.org> for submission details).

Important Dates

- **Deadline for submission of special session proposal;** **Ju1. 16, 2012**
- **Acceptance notice of special session proposal;** **Jul. 21, 2012**
- **Deadline for submission of regular session full paper;** **Aug. 11, 2012**
- **Deadline for submission of chip design contest;** **Aug. 25, 2012**
- **Deadline for submission of special session full paper;** **Aug. 25, 2012**
- **Notification of acceptance (all submitted papers);** **Sep. 08, 2012**
- **Deadline for final paper submission;** **Sep. 22, 2012**
- **Deadline for author and early-bird registration;** **Sep. 22, 2012**

At least one author of each accepted paper must register by September 22, 2012.

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